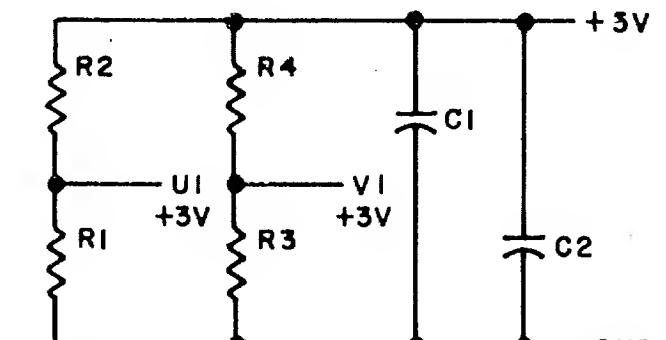
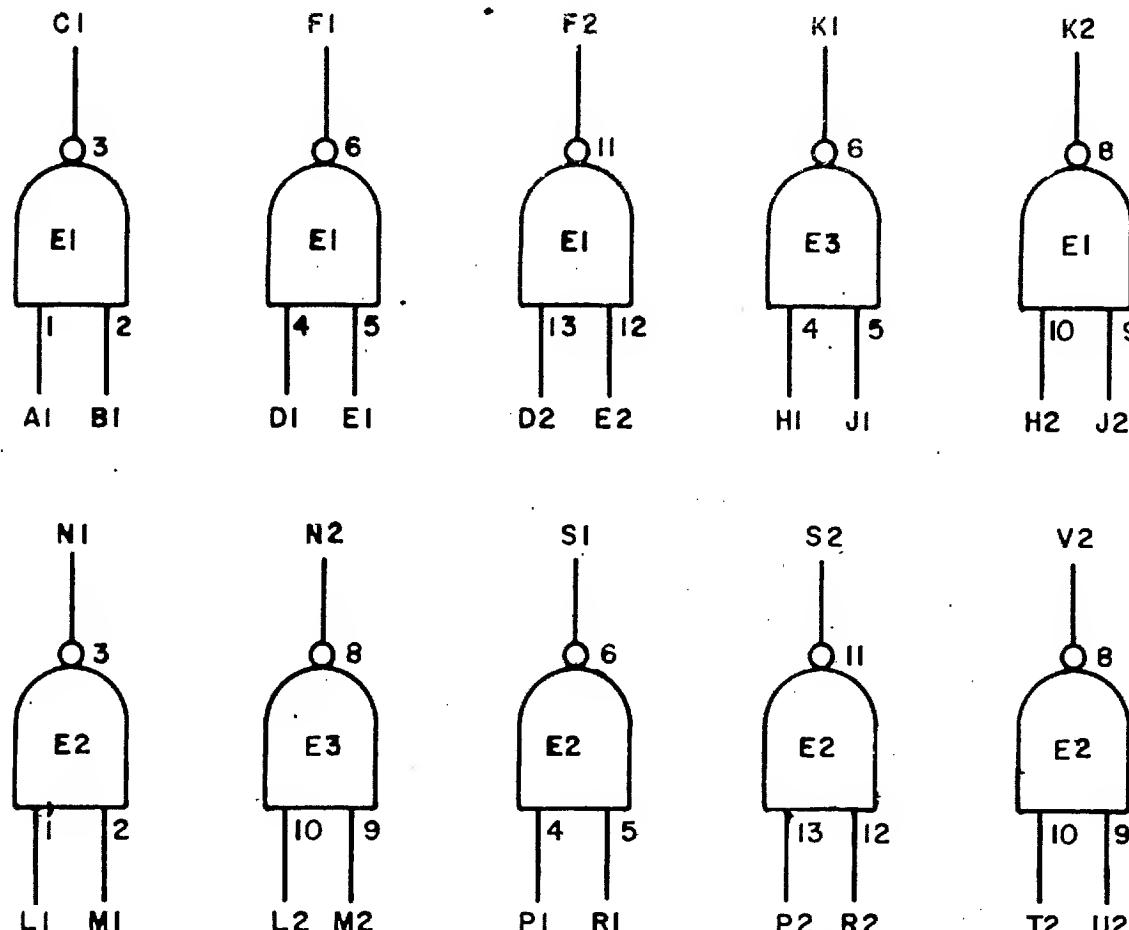


THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY.  
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+5V ————— A2

NOT USED -15V ————— B2

GND ————— C2, TI



NOTES:

PIN 7 ON EACH IC = GND

PIN 14 ON EACH IC = +5V

REFERENCE DESIGNATION	DESCRIPTION	PART NO.
PARTS LIST		

REVISIONS	DRN.	DATE	TRANSISTOR & DIODE CONVERSION CHART				digital		TITLE	10-2 INPUT NAND GATES MII3		
CHK	CHK NO.	REV.	DEC	EIA	DEC	EIA	EQUIPMENT CORPORATION		SIZE	CODE	NUMBER	REV.
6425	A	B					MAYNARD, MASSACHUSETTS		B	CS	MII3-0-1	C
6801	B	C					PRINTED CIRCUIT REV.		D			
00001												